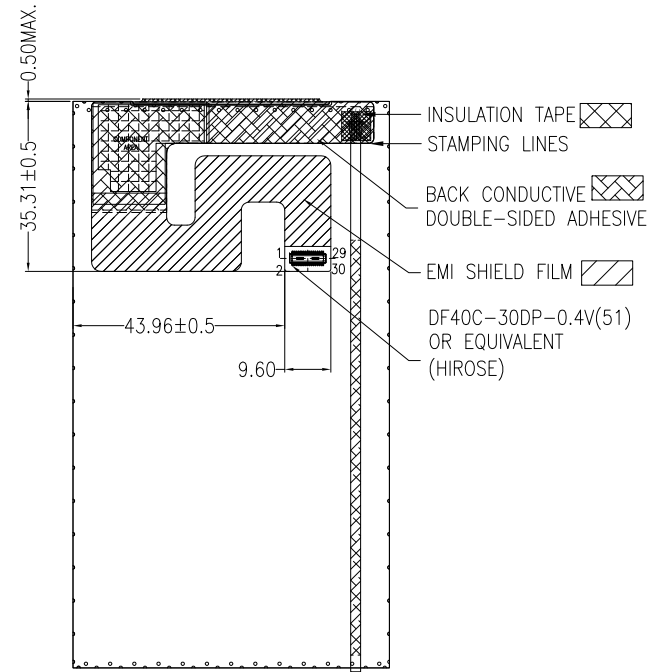
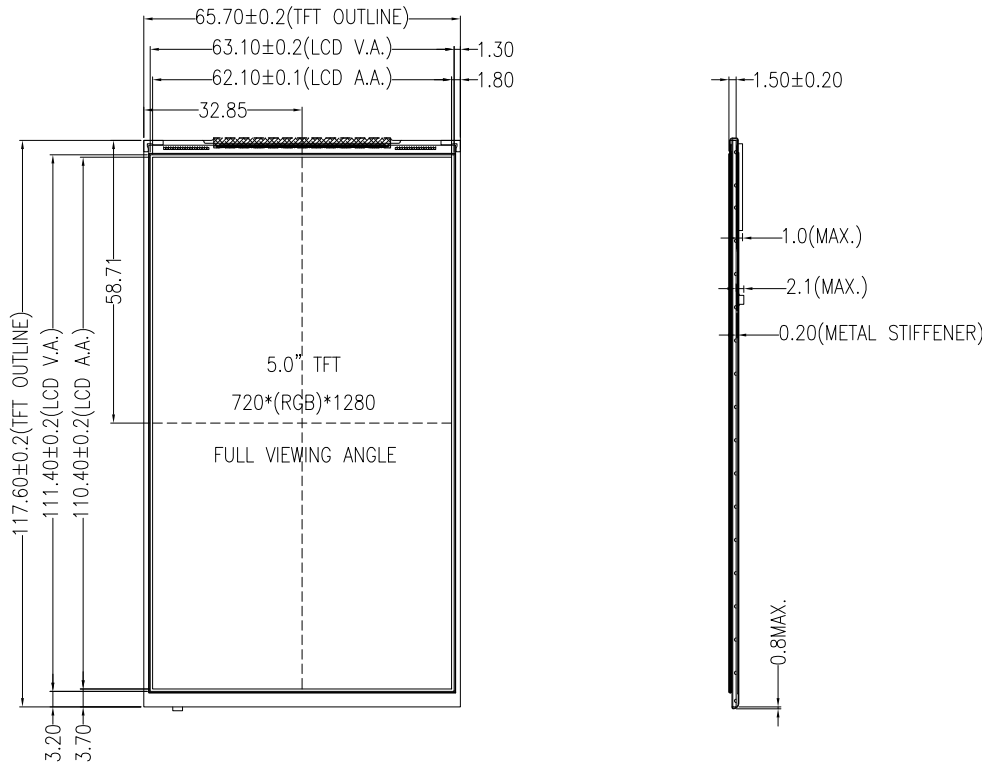
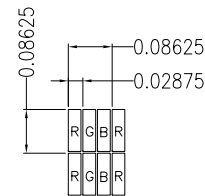




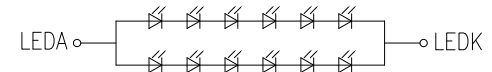
PIN DESCRIPTION	
PIN	SYMBOL
1	GND
2	GND
3	LANE3_P
4	LANE1_P
5	LANE3_N
6	LANE1_N
7	GND
8	GND
9	CLK_P
10	LANE2_P
11	CLK_N
12	LANE2_N
13	GND
14	GND
15	NC(TP_RESET)
16	LANE0_P
17	RESET
18	LANE0_N
19	TE
20	GND
21	VCI
22	VDDIO
23	VCI_EN
24	LEDK
25	NC(TP_INT)
26	NC
27	NC(TP_SDA)
28	LEDA
29	NC(TP_SCL)
30	NC

NOTES:

1. DISPLAY TYPE: LTPS FFS TFT, TRANSMISSIVE, NORMALLY BLACK
2. OPERATING VOLTAGE: VCI=2.8V (TYP.),VDDIO=1.8V (TYP.),VCI_EN=1.8V (TYP.)
3. VIEWING DIRECTION: FULL VIEWING ANGLE
4. DRIVER IC: FT7131M
5. OPERATING TEMP.: -20°C ~ 70°C
6. STORAGE TEMP.: -30°C ~ 80°C
7. LED BACKLIGHT: WHITE LED, IF=40mA, VF=16.8V~20.4V
8. SURFACE LUMINANCE: 500 CD/M² (TYP.)
9. TOP POLARIZER SURFACE TREATMENT: GLARE
10. RECOMMEND MATCHING CONNECTOR: HIROSE DF40C-30DS-0.4V(51) OR EQUIVALENT
11. GENERAL TOLERANCE: ±0.30MM
12. ROHS COMPLIANT



PIXEL DETAIL
(SCALE 100:1)



BACKLIGHT CIRCUIT DIAGRAM

MULTI-INNO TECHNOLOGY CO., LTD.						
DRAWN BY:	JERRY	2024.12.07		SCALE : 1/1	MODULE P/N:	
CHECKED BY:				UNIT: mm	MI0500ART-1	
APPROVED BY:				SHEET NO.: 1/1	DESCRIPTION:	
VERSION NO:	01				TFT MODULE	

01	FIRST ISSUE	JERRY	2024.12.07
VER.	REVISED DESCRIPTION	REVISER	DATE